Bailey network 90 NEWSLETTER

RT Thirtyacre
(FS-RPCC)
SE

Module Switch Settings

The attached sheets, developed by Bailey Canada, show the switch settings for the NETWORK 90 Modules. New modules, as they become available, will be added by supplement.

P. Handfield/D.J. Dziubakowski

PH/DJD:cp

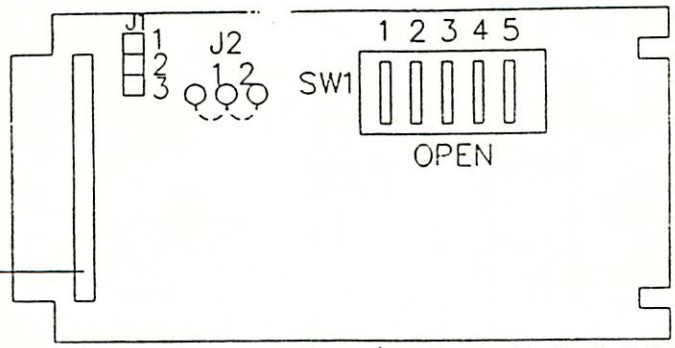
87-11
10-08-87

INDEX FOR MODULE SWITCH SETTINGS

GENERAL NOTES:

1. SWITCH POSITIONS "OPEN" AND "OFF" ARE EQUIVALENT.

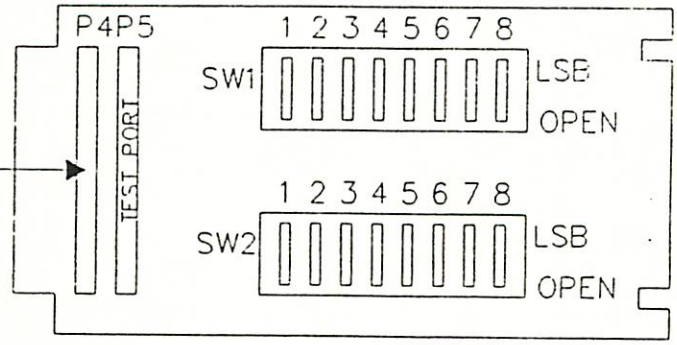
BUS AND LOOP INTERFACE MODULE SWITCH SETTINGS



NOTE: 1. SWITCH OPEN = LOGIC 1 (POS 5 IS THE LEAST SIGNIFICANT BIT)
2. SW1-5 IS SET TO THE MODULE ADDRESS (0 OR 1)
3. SW1-2 TO 4 NOT NORMALLY USED.
4. SW1-1 DEFINES REDUNDANT BIM'S SUPPLIED (0=NO, 1=YES).
5. J1 - ENABLE - DISABLE STATUS ALARM (1-2=ENABLE, 2-3=DISABLE)
6. J2 - SELECT MODE OF OPERATION (1=NBIM01A 2=NBIM02)

MODULE LOCATION	MODULE ADDRESS	SW1					J1	J2
		1	2	3	4	5	1/3	1/2
			0	0	0			

FLAT DMA CABLE (NBIM02 TO NLIM02 P4 CONNECTOR)



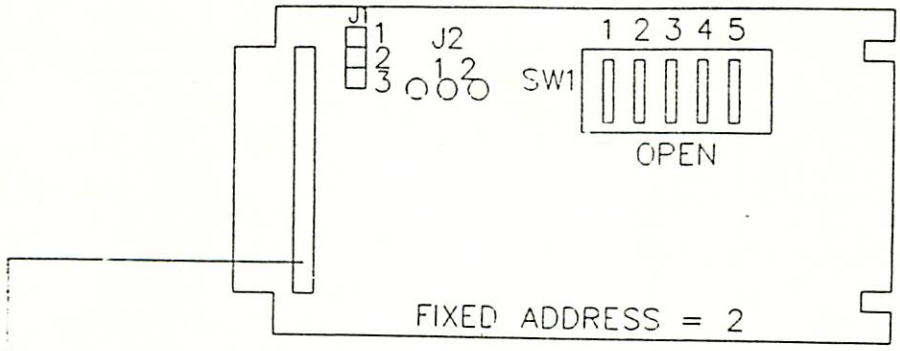
NOTE: 1. SWITCH OPEN = LOGIC 1 (POS 8 IS THE LEAST SIGNIFICANT BIT)
2. SW2 IS SET TO THE PCU ADDRESS (TO 63)
3. SW1 IS DIAGNOSTIC/DISPLAY SWITCH.
4. REFER TO SHT. 3 FOR STANDARD JUMPER SETTINGS.

MODULE LOCATION	MODULE ADDRESS	SW1 (TYPICAL)								SW2							
		1	2	3	4	5	6	7	8	1	2	3	4	5	6	7	8
		0	0	1	1	0	0	1	0	0	0						

REFER TO PRODUCT INSTRUCTION E93-908-1 FOR FURTHER INFORMATION

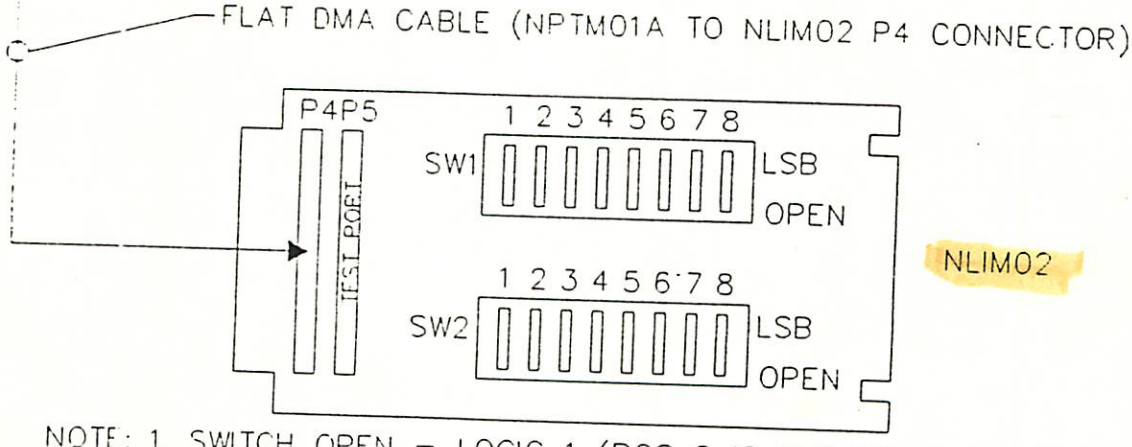
POINT TABLE AND LOOP INTERFACE MODULE SWITCH SETTINGS

PART OF CIU01



NOTE: 1. SW1 IS NOT NORMALLY USED (DIAGNOSTIC/DISPLAY LED'S)
2. J1 = ENABLE – DISABLE STATUS ALARM (1–2=ENABLE, 2–3=DISABLE)
3. J2 = NOT USED

MODULE LOCATION	MODULE ADDRESS	SW1					J1
		1	2	3	4	5	1/3
		X	0	0	0	X	



NOTE: 1. SWITCH OPEN = LOGIC 1 (POS 8 IS THE LEAST SIGNIFICANT BIT)
2. SW2 IS SET TO THE PCU ADDRESS (1 TO 63)
3. SW1 IS DIAGNOSTIC/DISPLAY SWITCH ONLY.
4. REFER TO SHT. 3 FOR STANDARD JUMPER SETTINGS.

MODULE LOCATION	SW1 (TYPICAL)								SW2							
	1	2	3	4	5	6	7	8	1	2	3	4	5	6	7	8
	0	0	1	1	0	0	1	0	0	0						

REFER TO PRODUCT INSTRUCTION E93-905 FOR FURTHER INFORMATION

MEMORY JUMPER SETTINGS

NLIM02

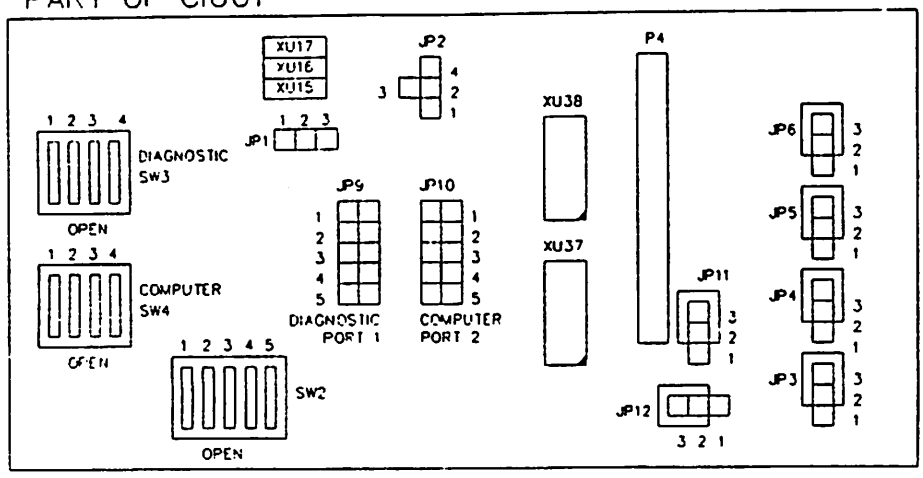
	STANDARD POSITION	ALTERNATE POSITION
CHIP TYPE=U4	25L32 2532/99401	2732
J1	2 + + 1 3 +	2 + + 1 3 +
J2	1 2 3 + + +	1 2 3 + + +

J3	ALWAYS INSERTED (USED FOR COMPUTERIZED TEST SETUP ONLY)

SERIAL INTERFACE MODULE SWITCH SETTINGS

PART OF CIU01

NSPM01



NOTES:

1. SWITCH SET OPEN = LOGIC 1
2. SW2 SELECT COMPUTER PORT ADDRESS.
3. SW3 SELECT PORT OPTIONS AND SW4 SELECTS PORT OPERATION OPTIONS.

SW3

NUMBER OF DATA BITS	NUMBER OF STOP BITS	TYPE OF PARITY	POLE NUMBER *1	2	3	4
7	2	EVEN	0	0	0	
7	2	ODD	0	0	1	
7	1	EVEN	0	1	0	
7	1	ODD	0	1	1	
8	2	NO	1	0	0	
8	1	NO	1	0	1	
8	1	EVEN	1	1	0	
8	1	ODD	1	1	1	

* POLE 1 OF SW3 IS NOT USED

SW4

OPTIONS	POLE NUMBER 1	2	3	4*
NORMAL MODE	0			
ECHO CHECK MODE	1			
COMMAND & REPLAY CHECKSUMS		0		
NO COMMAND & REPLY CHECKSUMS		1		
SIM COMPUTER PORT ADDRESS SELECT				
PROTOCAL DISABLED			0	
PROTOCAL ENABLED			1	
MOST SIGNIFICANT BIT OF SIM COMPUTER PORT ADDRESS				X

X = DON'T CARE

SW2

COMPUTER PORT ADDRESS DEFINITION	POLE NUMBER 1	2	3	4	5
ADDRESS = (SW4 POLE 4) & (SW2 POLES 1-5)	X	X	X	X	X

ONLY VALID WHEN SW4 POLE 3 = 1

4. JP1 AND JP2 SELECT ROM TYPES.

JUMPER	XU15 ONLY #2732	XU15 ONLY #2532	XU15,16,17 #2564	XU15,16,17 68764
JP1	1 2 3 [+ +] +	1 2 3 + [+ +]	1 2 3 + [+ +]	1 2 3 + [+ +]
JP2	+ 4 3 + [+] 2 [+] 1	+ 4 3 [+ +] 2 + 1	[+] 4 3 + [+] 2 + 1	[+] 4 3 + [+] 2 + 1

5. JP3 TO JP6 AND JP9 TO JP12 OPTIONS.

OPTION	PORT 1 / PORT 2	JP3 / JP5	JP4 / JP6	JP11 / JP12
NSIM01 MONITORS RTS LINE		PIN 1 TO PIN 2		
NSIM01 DOES NOT MONITOR RTS LINE		PIN 2 TO PIN 3		
NSIM01 MONITORS DTR LINE			PIN 1 TO PIN 2	
NSIM01 DOES NOT MONITOR DTR LINE			PIN 2 TO PIN 3	
NSIM01 PROVIDES CTS/DSR SIGNAL				PIN 1 TO PIN 2
CTS/DSR LINE SET TO +15V				PIN 2 TO PIN 3

	BAUD	JP9 (PORT 1)		JP10 (PORT 2)	
1	19,200	O	O	O	O
2	9,600	O	O	O	O
3	2,400	O	O	O	O
4	1,200	O	O	O	O
5	300	O	O	O	O

REFER TO PRODUCT INSTRUCTIONS E93-905 FOR FURTHER INFORMATION

NETWORK 90

Bailey CANADA

7/N00175

AL760273A SHT. 4

CAD FILE - 7J080824

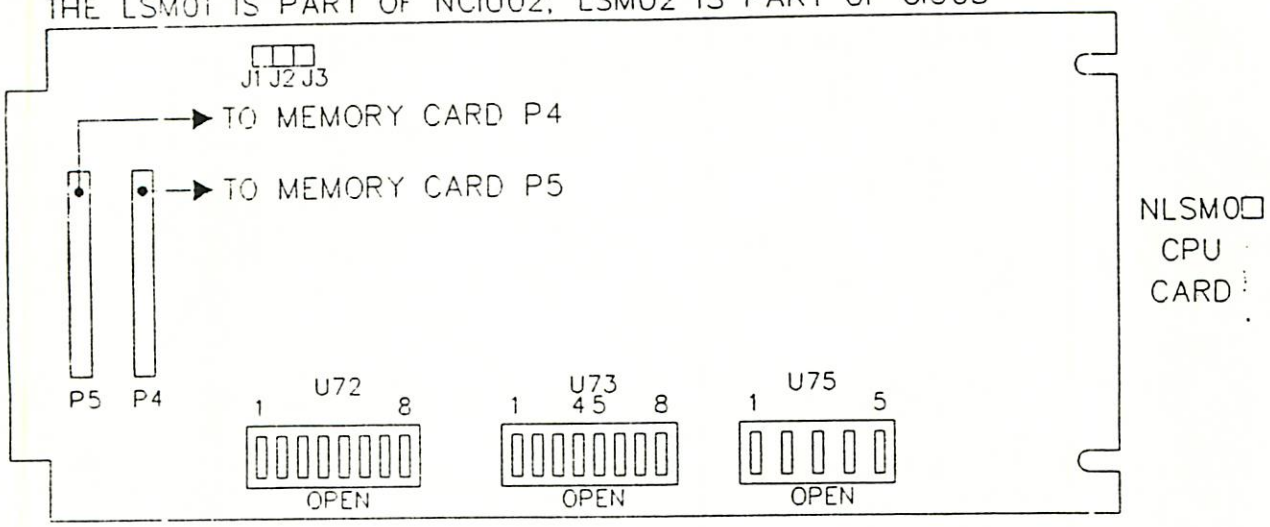
SERIAL INTERFACE MODULE SWITCH SETTINGS. CONTINUED

TYPICAL SETTINGS

MODULE	SW2					SW3				SW4				JP9	JP10	JP1	JP2	JP3	JP4	JP5	JP6	JP11	JP12
LOCATION	1	2	3	4	5	1	2	3	4	1	2	3	4	1 TO 5	1 TO 5	1/3	1/4	1/3	1/3	1/3	1/3	1/3	1/3
	0	0	0	0	0	0	1	0	1	0	0	0	1			3	3	3	3	3	3	3	3

LOOP STORAGE MODULE SWITCH SETTINGS

THE LSM01 IS PART OF NCIU02; LSM02 IS PART OF CIU03



NOTES:

1. SWITCH SET "OPEN" = 1
2. SWITCH U75 SELECTS PORT ADDRESS:
   POS'N 5 IS LEAST SIGNIFICANT BIT
3. J1-2-3 SELECTS EPROM TYPE INSTALLED J1-J2 = 27256
   J2-J3 = 27128
4. SWITCH U73 SELECTS TRANSMISSION RATES FOR TWO RS232 PORTS:
   POS'N 1 TO 4 = TERMINAL PORT (POS'N 1 = LSB)
   POS'N 5 TO 8 = PRINTER PORT (POS'N 5 = LSB)

BAUD RATES:				
0 = 50	4 = 150	8 = 1.8K	12 = 4.8K
1 = 75	5 = 300	9 = 2K	13 = 7.2K
2 = 110	6 = 600	10 = 2.4K	14 = 9.6K
3 = 134.5	7 = 1.2K	11 = 3.6K	15 = 19.2K

5. SWITCH U72 SELECTS OPERATING OPTIONS:
   POLE 1 = ROM CHECKSUMMMING, 1 = OFF; 0 = ON
   POLES 2 & 3 = PORT 0 & 1 DATA CHARACTERISTICS
   00 -8 BITS, 1 STOP BIT, NO PARITY
   01 -8 BITS, 1 STOP BIT, EVEN PARITY
   10 -8 BITS, 1 STOP BIT, ODD PARITY
   11 -8 BITS, 2 STOP BITS.
   POLE 4 = PORT 1 OPTION (1 = UTILITY/0 = COMPUTER)
   POLE 5 = MODEM PASSWORD PROTECTION (1 = ON/0 = OFF)
   POLE 6 = PORT ADDRESSING MODE (1 = ON/0 = OFF)
   POLE 7 = CHECKSUMMING (1 = ON/0 = OFF)
   POLE 8 = PRIMARY/SECONDARY (DIFFERENT BETWEEN REDUNDANT LSM'S)

MODULE LOCATION	U75					U72								U73								J1-J2-J3
	1	2	3	4	5	1	2	3	4	5	6	7	8	1	2	3	4	5	6	7	8	J1-J2 OR J2-J3

REFER TO PROGRAMMER'S REFERENCE MANUAL E93-905-2 FOR FURTHER INFORMATION.

NETWORK 90

Bailey CANADA

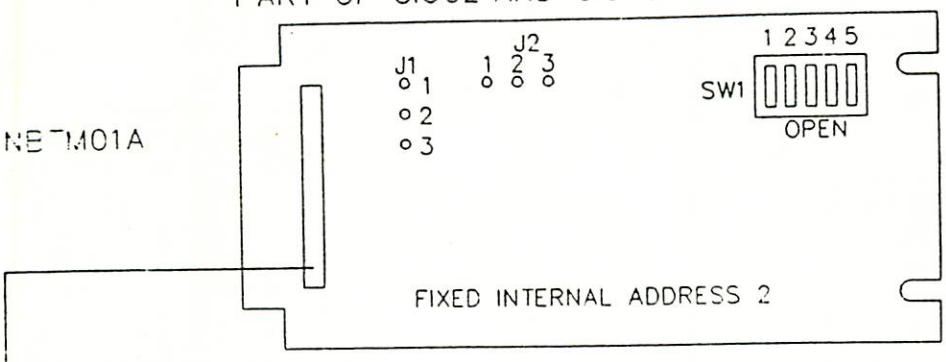
7/N00175

AL760273A SHT. 6
CAD FILE- 7J080840

M Schneider

BUS TRANSFER AND LOOP INTERFACE MODULE SWITCH SETTINGS

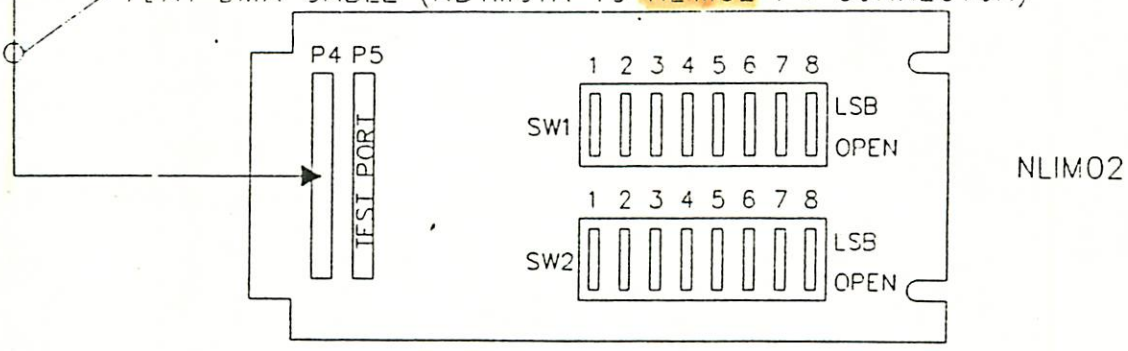
PART OF CIU02 AND CIU03



NOTES:

1. SWITCH SET OPEN = LOGIC 1
2. SW1 IS NOT NORMALLY USED (DIAGNOSTICS)
3. J1 – ENABLE/DISABLE STATUS ALARM (1–2=ENABLE, 2–3=DISABLE)
4. J2 – NOT USED

MODULE LOCATION	MODULE ADDRESS	SW1					J1
		1	2	3	4	5	1/3
	2	0	0	0	0	0	



NOTS:

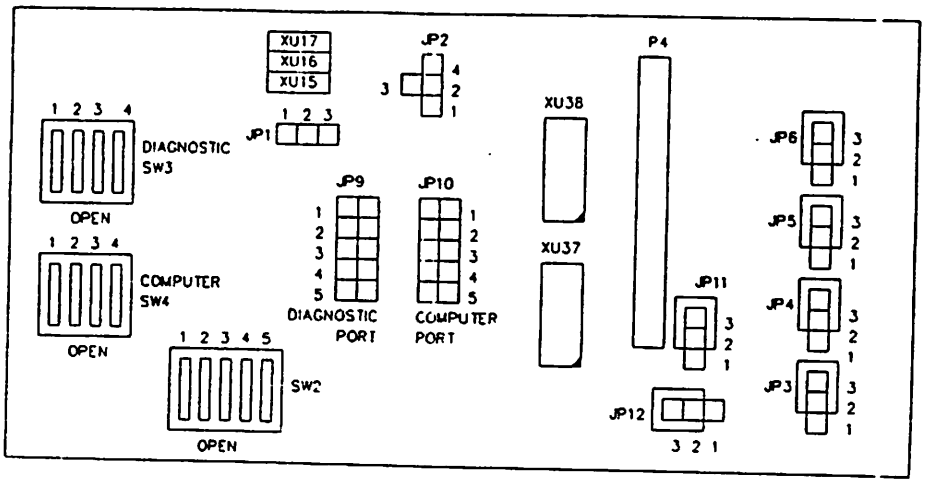
1. SWITCH OPEN = LOGIC 1 (POS 8 IS THE LEAST SIGNIFICANT BIT)
2. SW2 IS SET TO THE PCU ADDRESS (1 TO 63)
3. SW1 IS DIAGNOSTIC/DISPLAY SWITCH
4. REFER TO SHT. 3 FOR STANDARD JUMPER SETTINGS.

MODULE LOCATION	SW1(TYPICAL)								SW2							
	1	2	3	4	5	6	7	8	1	2	3	4	5	6	7	8
	0	0	1	1	0	0	1	0	0	0						

REFER TO PRODUCT INSTRUCTION E93-905-2 FOR FURTHER INFORMATION

SERIAL PORT MODULE SWITCH SETTINGS

NSPM01



NOTES:

1. SWITCH SET OPEN = LOGIC 1
2. SW2 SELECT ADDRESS (5=LSB)
3. SW3 SELECT PORT OPTIONS AND SW4 SELECTS PORT OPERATION OPTIONS.

SW3

NUMBER OF DATA BITS	NUMBER OF STOP BITS	TYPE OF PARITY	POLE NUMBER *1	2	3	4
7	2	EVEN	0	0	0	
7	2	ODD	0	0	1	
7	1	EVEN	0	1	0	
7	1	ODD	0	1	1	
8	2	NO	1	0	0	
8	1	NO	1	0	1	
8	1	EVEN	1	1	0	
8	1	ODD	1	1	1	

* POLE 1 OF SW3 IS USED TO SELECT THE FOLLOWING.
1- WITH SW3 IN THIS POSITION FRAMING AND PARITY OPTIONS OF BOTH PORTS 1 AND 2 ARE SET BY SW3 ACCORDING TO TABLE 3
0- PORT 1 (DIAGNOSTIC PORT) FRAMING AND PARITY OPTIONS ARE SET BY SW3 ACCORDING TO TABLE 3 BUT PORT 2 FRAMING OPTIONS ARE SET TO 8 DATA BITS, 1 STOP BIT, AND EVEN PARITY.

SW4

OPTIONS	POLE NUMBER 1	2	3	4
BINARY CODE	0			
ASCII CODE	1			
ROM SELF TEST DURING NORMAL OPERATION		0		
NOT ALLOWED		1		
NORMAL OPERATION			0	
ECHO CHECK			1	
COMMAND AND REPLY CHECKSUMS USED				0
COMMAND AND REPLY CHECKSUMS NOT USED				1

4. JP1 AND JP2 SELECT ROM TYPES.

JUMPER	XU15 ONLY #2732	XU15 ONLY #2532	XU15,16,17 #2564	XU15,16,17 68764
JP1	1 2 3	1 2 3	1 2 3	1 2 3
JP2	4 3 2 1	4 3 2 1	4 3 2 1	4 3 2 1

5. JP3 TO JP6 AND JP9 TO JP12 OPTIONS.

OPTION	PORT 1 / PORT 2	JP3 / JP5	JP4 / JP6	JP11 / JP12
NSPM01 MONITORS RTS LINE		PIN 1 TO PIN 2		
NSPM01 DOES NOT MONITOR RTS LINE		PIN 2 TO PIN 3		
NSPM01 MONITORS DTR LINE			PIN 1 TO PIN 2	
NSPM01 DOES NOT MONITOR DTR LINE			PIN 2 TO PIN 3	
NSPM01 PROVIDES CTS/DSR SIGNAL				PIN 1 TO PIN 2
CTS/DSR LINE SET TO +15V				PIN 2 TO PIN 3

	BAUD	JP9 (PORT 1)	JP10 (PORT 2)
1	19,200	○ ○	○ ○
2	9,600	○ ○	○ ○
3	2,400	○ ○	○ ○
4	1,200	○ ○	○ ○
5	300	○ ○	○ ○

REFER TO PRODUCT INSTRUCTIONS E93-905-1 FOR FURTHER INFORMATION

NETWORK 90

Bailey CANADA

AL760273 SHT 8
CAD FILE- 7J080845

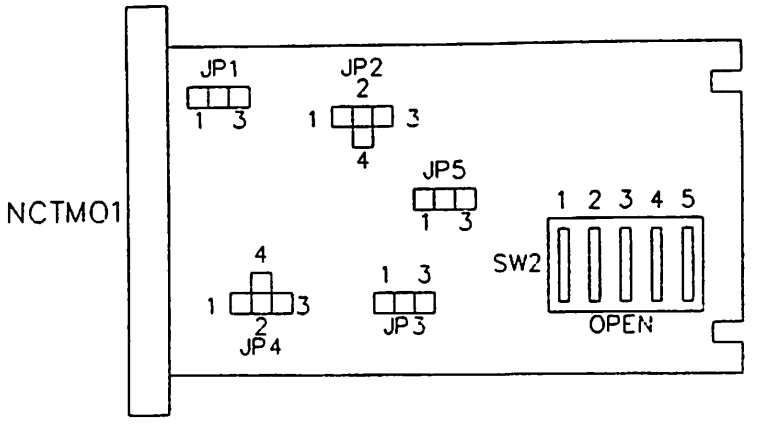
7/N00175

SERIAL PORT MODULE SWITCH SETTINGS. CONTINUED

TYPICAL SETTINGS

MODULE	SW2					SW3				SW4				JP9	JP10	JP1	JP2	JP3	JP4	JP5	JP6	JP11	JP12
LOCATION	1	2	3	4	5	1	2	3	4	1	2	3	4	1 TO 5	1 TO 5	1/3	1/4	1/3	1/3	1/3	1/3	1/3	1/3
						1	1	0	1	0	1	0	0			3	3	3	3	3	3	3	3

CONFIGURATION AND TUNING MODULE SWITCH SETTINGS



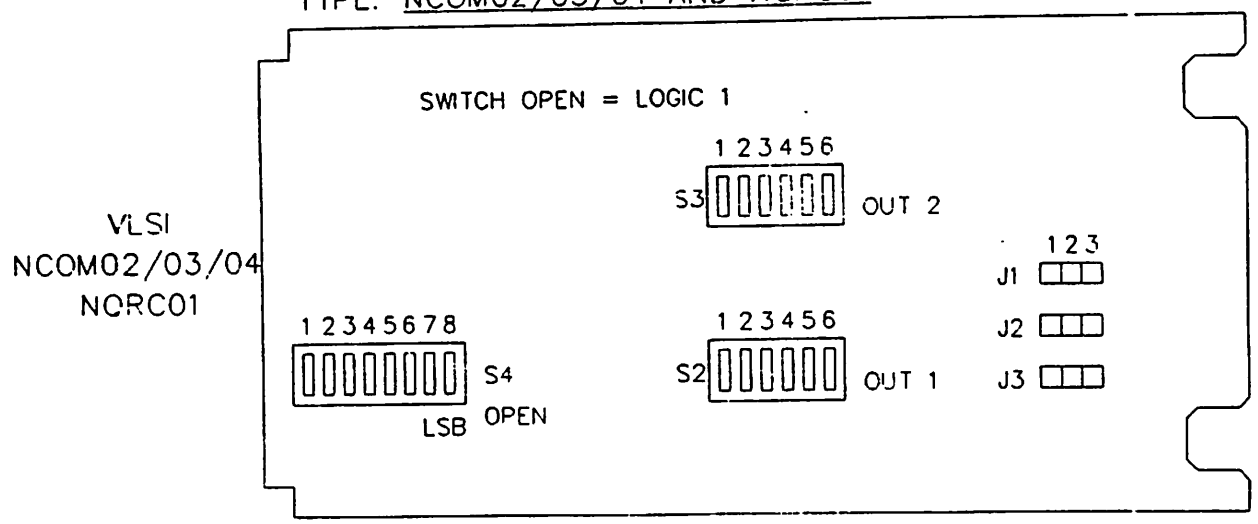
NOTE: 1) SWITCH OPEN = LOGIC 1 (POS 5 IS THE LEAST SIGNIFICANT BIT)
2) SW2 IS THE MODULE ADDRESS WHICH IS NORMALLY SET TO 2

MODLUE ADDRESS	SW2				
	1	2	3	4	5
TYPICAL	0	0	0	1	0
ACTUAL					

JUMPER TABLE			
JUMPER	POSITIONS CLOSED STD. (25L32) ALT. (2532)	ALT. (2732)	COMP. DES.
JP1	2 – 3	1 – 2	U22
JP2	1 – 2	2 – 3	
JP3	2 – 3	1 – 2	U21
JP4	1 – 2	2 – 3	
JP5	1 – 2	1 – 2	U21, U22

VLSI CONTROLLER MODULE SWITCH SETTINGS

TYPE: NCOM02/03/04 AND NORC01



NOTES: 1) SWITCH OPEN = LOGIC 1

2) S4 ADDRESS SWITCH (POSITION 8 IS LEAST SIGNIFICANT)
   S4-1 TO S4-3 SELF TEST - NORMALLY CLOSED
   S4-4 TO S4-8 ADDRESS (3-31)

3) S2 AND S3 ANALOG OUPUT CONFIGURATION
   S2=OUTPUT #1 S3=OUTPUT #2
   S_-1 OPEN 5.25V ON POWER-UP
   CLOSED 0.75V ON POWER-UP
   S_-2,3 2 OPEN AND 3 CLOSED HOLD LAST VALUE
   2 CLOSED AND 3 OPEN POWER-UP SETTING
   S_-4,5,6 4 AND 6 CLOSED AND 5 OPEN VOLTAGE OUTPUT
   4 AND 6 OPEN AND 5 CLOSED CURRENT OUTPUT

CAUTION:
USER MUST VERIFY THAT SWITCHES S2 & S3 ARE PROPERLY SET UP FOR CURRENT/VOLTAGE ALONG WITH T.U. DIPSHUNT CONFIGURATION PRIOR TO POWER-UP, OTHERWISE POSSIBLE DAMAGE TO MODULE COULD OCCUR. FOR NON-CONFIGURED SYSTEMS ENSURE THAT S2 & S3 ARE ALL OPEN.

4) J1, J2, J3 SELECT DIGITAL INPUT VOLTAGE (1-2=24VDC, 2-3=125VDC)

MODULE ADDRESS	MODULE LOCATION	S4								S2						S3						J1	J2	J3
		1	2	3	4	5	6	7	8	1	2	3	4	5	6	1	2	3	4	5	6	1/3	1/3	1/3
		0	0	0																				

FOR FURTHER INFORMATION ON NCOM02/03/04 SEE PRODUCT INSTRUCTION E93-906-4.
FOR FURTHER INFORMATION ON NORC01 SEE PRODUCT INSTRUCTION E93-906-3.

NETWORK 90

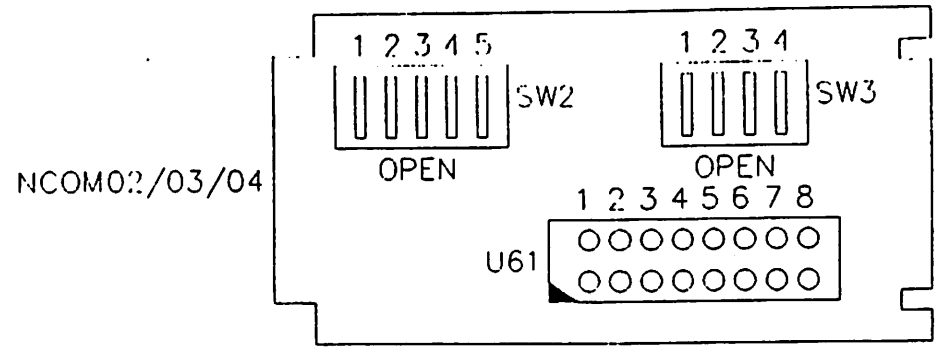
Bailey CANADA

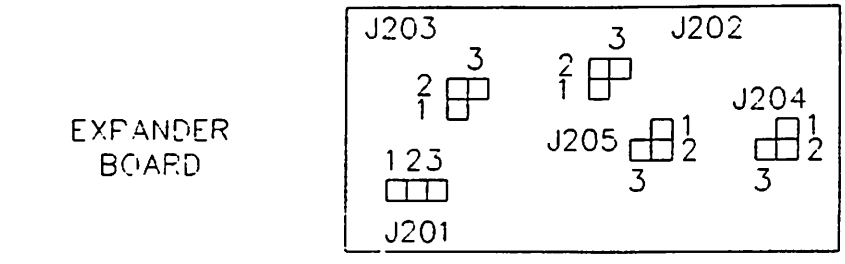
7/N00175

AL760273A SHT. 11
CAD FILE- 7J080832 MSchroeder

CONTROLLER MODULE SWITCH SETTINGS

TYPE: NCOM02/03/04





MODULE ADDRESS	SW2					SW3				U61								EXPANDER J20X (1/3)				
	1	2	3	4	5	1	2	3	4	1	2	3	4	5	6	7	8	1	2	3	4	5
						0	0		0													

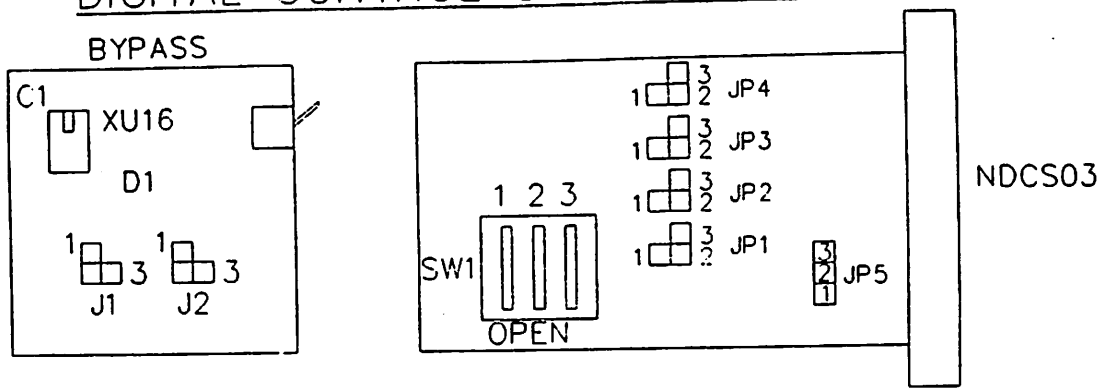
NOTE: 1. SWITCH OPEN = LOGIC 1 (POS. 5 IS THE LEAST SIGNIFICANT BIT)
2. SW2 IS SET TO THE MODULE ADDRESS (3 THRU 31)
3. SW3-3 SELECTS POWER-UP OPTION FOR AO#1 (0=0%, 1=100%)
SW3-1, 2, 4 ARE NORMALLY CLOSED
4. U61-1, 2, 3 SELECTS TYPE OF AO#1 (010 = CURRENT: 101 = VOLTAGE)
5. U61-4, 5, 6 SELECTS TYPE OF AO#2 (010 = CURRENT: 101 = VOLTAGE)
6. U61-7 + 8 SELECTS TIME OUT OPTION FOR AO#1

AO#1	7	8
TO HOLD	1	0
TO POWER-UP	0	1

7. J201, 2, 3 SELECT DIGITAL INPUT VOLTAGE (1 = 24VDC, 3 = 125VDC)
8. J204 SELECTS TIME-OUT OPTION FOR AO#2 (1 = HOLD, 3 = POWER-UP)
9. J205 SELECTS POWER-UP OPTION FOR AO#2 (1 = 100%, 3 = 0%)
10. SW3-1 IS USED TO ZERO MEMORY

REFER TO PRODUCT INSTRUCTION E93-906-2 FOR FURTHER INFORMATION

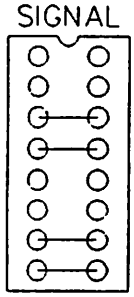
DIGITAL CONTROL SWITCH SETTINGS

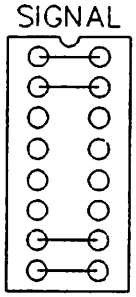


NOTE: 1. SWITCH OPEN = LOGIC 1 (POS 3 IS THE LEAST SIGNIFICANT BIT)
2. SW1 IS SET TO THE STATION ADDRESS (0 TO 7)
3. BY-PASS CARD – XU16 DIPSHUNT (SEE BELOW)
– J1 AND J2 BOTH V.
4. JP1 TO JP5 SELECT OPTIONS PER TABLE.
5. JP3 SELECTS VALUE TO BE DISPLAYED ON OUTPUT METER DURING NORMAL OPERATION.
6. JP4 SELECTS ON WHICH METER THE ANALOG INPUT IS DISPLAYED DURING BYPASS.

JUMPER	SELECT	OPTION
JP1	1–2 2–3	AUTO BYPASS CANCEL AUTO BYPASS
JP2	1–2 2–3	REVERSE ACTING CANCEL REVERS ACTING
JP3	1–2 2–3	COM OUTPUT DISPLAYED (SEE NOTE 5) ANALOG INPUT DISPLAYED
JP4	1–2 2–3	ANALOG INPUT DISPLAYED ON CO SCALE (SEE NOTE 6) ANALOG INPUT DISPLAYED ON PV SCALE
JP5	1–2 2–3	BRIGHT INTENSITY SELECTED DIM INTENSITY SELECTED

XU16– FOR ANALOG SIGNALS ONLY
(IF DCS03 IS USED FOR DRIVER REFER TO INSTRUCTION BOOK)





NOTE:
XU8 ON T.U. TYPE NTCS0☐
MUST ALSO BE CONFIGURED IN
CONJUNCTION WITH THIS.
(REFER DL780010)

REFER TO PRODUCT INSTRUCTION E93-902-1 FOR FURTHER INFORMATION.

NETWORK 90

Bailey CANADA

7/N00175

AL760273A SHT. 13
CAD FILE– 7J080825

DIGITAL CONTROL STATION SWITCH SETTINGS (CON'T)

NDCS03

TAG NO.	STATION SERVICE	STA. ADD.	SW1 1	SW1 2	SW1 3	JP1 1/3	JP2 1/3	JP3 1/3	JP4 1/3	JP5 1/3	CONN. TO P2 OR P3	TU LOC	BYPASS XU16 NOTE 1	BYPASS J1 1/3	BYPASS J2 1/3

NOTE 1: I = 4-20mA ANALOG INPUT INTERNAL
E = 4--20mA ANALOG INPUT EXTERNAL
U = UNIVERSAL ACUTUATOR ANALOG INPUT EXTERNAL
P = RW DRIVE ANALOG INPUT INTERNAL

AL760273A SHT. 14
CAD FILE- 7J080826

NETWORK 90

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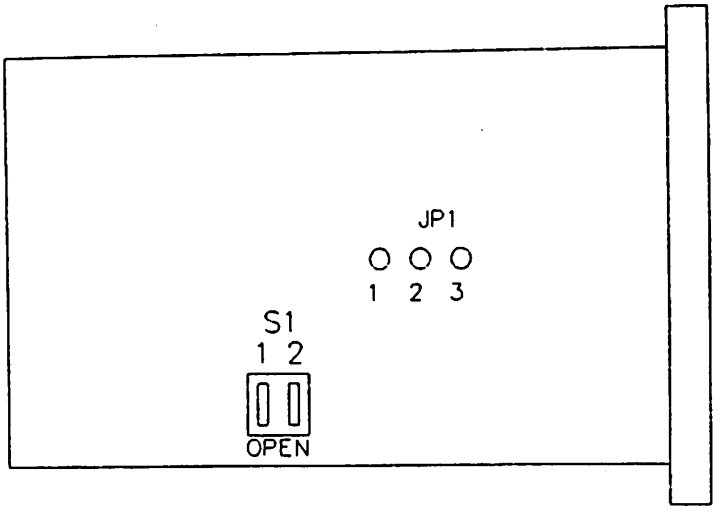
7/N00175

DIGITAL INDICATING STATION SWITCH SETTINGS



NOTES:

1. SWITCH SET OPEN = LOGIC 1
2. S1 SELECTS ADDRESS (2 = LSB)

STATION ADDRESS	S1-1	S1-2
8	0	0
9	0	1
10	1	0
11	1	1

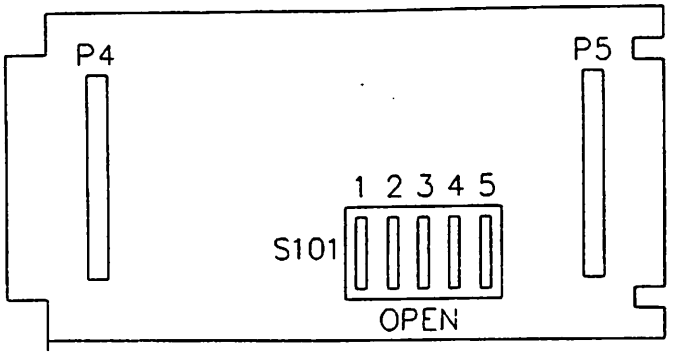
3. JP1 SELECT DISPLAY INTENSITY
   1 = BRIGHT DISPLAY
   3 = DIM DISPLAY

TAG NO.	STATION SERVICE	STA ADD	S1		JP1	TU LOC	CONNECTED TO P2 OR P3
			1	2	1/3		

REFER TO PRODUCT INSTRUCTION E93-904-2 FOR FURTHER INFORMATION

ANALOG MASTER MODULE SWITCH SETTINGS

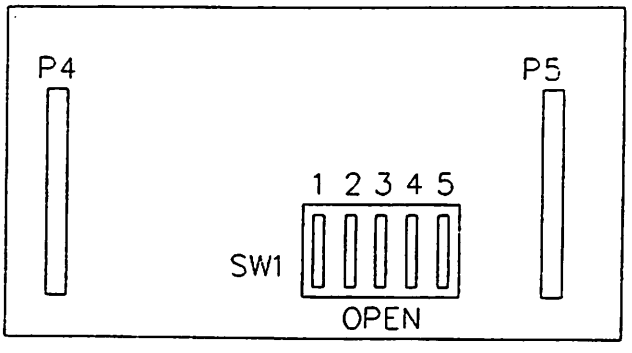
NAMM02
MOTHER
BOARD



NOTES: 1) SWITCH OPEN = LOGIC 1 (POS 5 IS THE LEAST SIGNIFICANT BIT)
2) S101 IS SET TO THE MODULE ADDRESS (3 TO 31)

MODULE LOCATION	MODULE ADDRESS	S101				
		1	2	3	4	5

NAMM02
MASTER ANALOG
BOARD

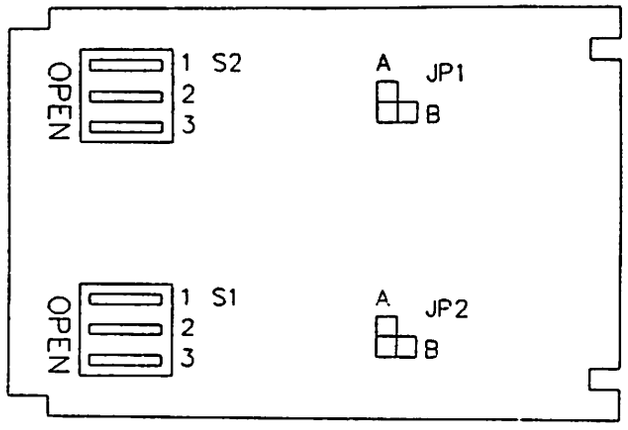


NOTES: 1) SWITCH OPEN = LOGIC 1 (POS 5 IS THE LEAST SIGNIFICANT BIT)
2) SW1 ON "ANALOG BOARD" SET FOR EAROM INITIALIZE MODE.

INITIALIZE	1	2	3	4	5
CONFIG. EAROM	0	0	0	1	0
CALIB. EAROM	0	0	0	1	1
BOTH EAROMS	0	0	1	0	0

REFER TO PRODUCT INSTRUCTION E93-912-2 FOR FURTHER INFORMATION

HIGH LEVEL ANALOG SLAVE SWITCH SETTINGS



NOTES: 1) SWITCH OPEN = LOGIC 1 (POS 3 IS THE LEAST SIGNIFICANT BIT)
2) JP1 SELECTS GROUP A, JP2 GROUP B (A=DISABLE, B=ENABLE)
3) S1 IS SET TO GROUP B ADDRESS, S2 TO GROUP A ADDRESS

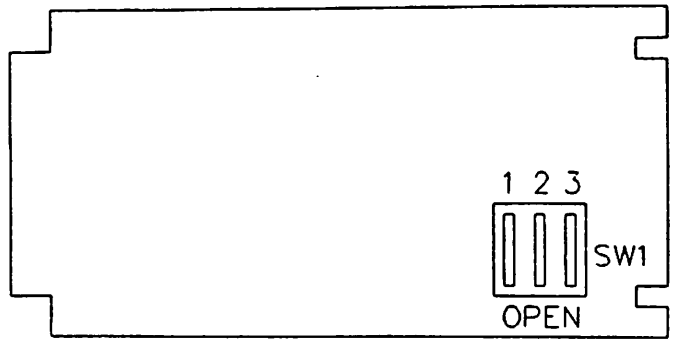
MASTER ADDRESS	SLAVE #	SLAVE ADD GROUP A	S2			SLAVE ADD GROUP B	S1			JP1 A/B	JP2 A/B
			1	2	3		1	2	3		
	1										
	2										
	3										
	4										

REFER TO PRODUCT INSTRUCTION E93-912-4 FOR FURTHER INFORMATION

LOW LEVEL ANALOG SLAVE MODULES SWITCH SETTINGS

TYPE: NASM02/NASM03/NASM04

SLAVE
NASM02/03/04



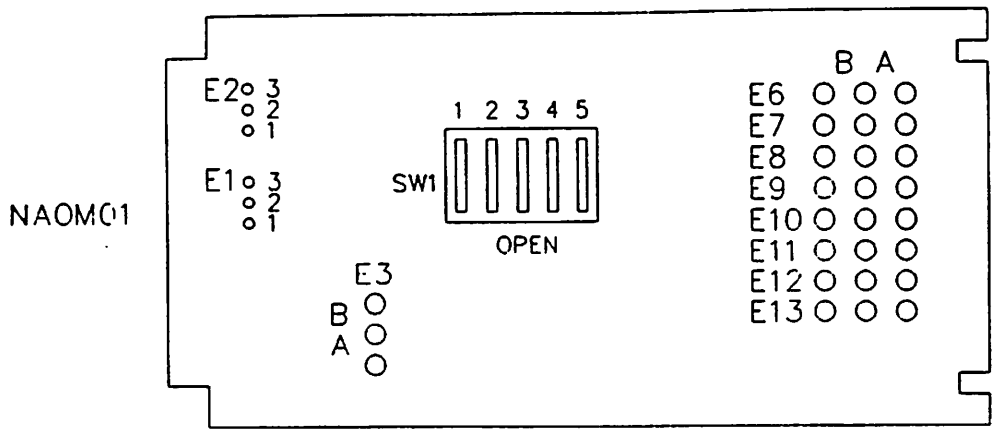
NOTES: 1) SWITCH OPEN = LOGIC 1 (POS 3 IS THE LEAST SIGNIFICANT BIT)
2) S1 IS SET TO SLAVE ADDRESS (0 TO 7)

MASTER ADDRESS	MASTER LOCATION	SLAVE ADDRESS	SLAVE LOCATION	SW1			SLAVE TYPE
				1	2	3	SEE TABLE 1

TABLE 1

NOMENCLATURE	DESCRIPTION	PRODUCT INSTRUCTION #
NASM02	THERMOCOUPLE/mVOLT	E93-912-5
NASM03	100 Ω RTD	E93-912-6
NASM04	10 Ω RTD	

ANALOG OUTPUT MODULE SWITCH SETTINGS



NOTES: 1) SWITCH OPEN = LOGIC 1 (POS 5 IS THE LEAST SIGNIFICANT BIT)
2) SW1 IS SET TO THE MODULE ADDRESS (3 TO 31)
3) E3 IS THE VOLTAGE RANGE (A = 1–5V/4–20mA, B = 0–10V)
4) E6–E13 SETS THE TYPE OF OUTPUT (A = VOLTAGE, B = CURRENT)

ADD	SW1					E3	E6	E7	E8	E9	E10	E11	E12	E13
	1	2	3	4	5	A/B	AO8	AO7	AO6	AO5	AO4	AO3	AO2	AO1

MEMORY TYPE USED	E1 AND E2
2532	2 & 3
25L32	2 & 3
2732	1 & 2
90401	2 & 3

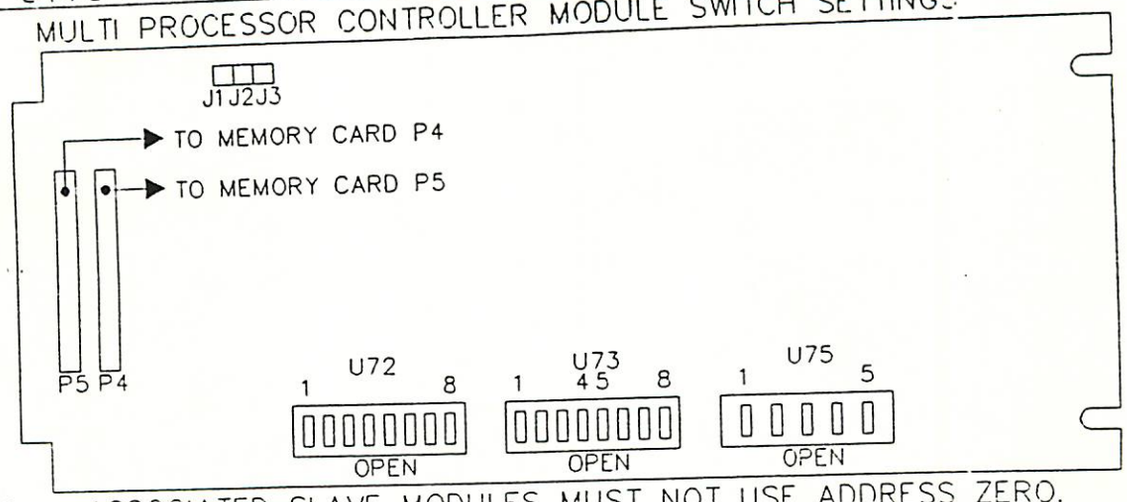
OUTPUT NO.	USED BY BLOCK	DRAWING
AO1		
AO2		
AO3		
AO4		
AO5		
AO6		
AO7		
AO8		

REFER TO PRODUCT INSTRUCTION E93-912-3 FOR FURTHER INFORMATION

MULTI-FUNCTION CONTROLLER MODULE SWITCH SETTINGS

MULTI PROCESSOR CONTROLLER MODULE SWITCH SETTINGS

NMFC01
NMFC02
NMFC03
NMPC01
CPU
CARD



NOTE: -ASSOCIATED SLAVE MODULES MUST NOT USE ADDRESS ZERO.
-SLAVE ADDRESS 01 HAS LOWEST PRIORITY (AND 63 HIGHEST) RELATIVE TO SLAVE INTERRUPTS TO MFC.

1. SWITCH SET "OPEN" = LOGIC 1
2. SWITCH U75 SELECTS MODULE ADDRESS (POSITION 5 = LSB)
   NOTE: REDUNDANT MFC APPLICATIONS USE SAME ADDRESS.
3. J1-2-3 SELECTS EPROM TYPE INSTALLED J1 - J2 = 27256
   J2 - J3 = 27128
4. SWITCH U73 SELECTS TRANSMISSION RATES FOR TWO RS232 PORTS:
   POS'N 1 TO 4 = TERMINAL PORT (POS'N 1 = LSB)
   POS'N 5 TO 8 = PRINTER PORT (POS'N 5 = LSB)

BAUD RATES:			
0 = 50	4 = 150	8 = 1.8K	12 = 4.8K
1 = 75	5 = 300	9 = 2K	13 = 7.2K
2 = 110	6 = 600	10 = 2.4K	14 = 9.6K
3 = 134.5	7 = 1.2K	11 = 3.6 K	15 = 19.2K

5. SWITCH U72 SELECTS OPERATING OPTIONS:

POS'N
1 = NOT USED
2 = ON-LINE CONFIGURATION = OPEN
3 = INHIBIT E²ROM CHECK = OPEN
4 = INHIBIT ROM CHECK(MEMORY) = OPEN
5 = INHIBIT ROM CHECK(CPU) = OPEN
6 = COMPACT E²ROM FREE SPACE = OPEN
7 = INITIALIZE E²ROM = OPEN
8 = MASTER/SLAVE MFC (OPEN = SLAVE)

} NORMALLY SET CLOSED

MODULE ADDRESS	MODULE LOC	U75					U72								U73								J1,J2,J3
		1	2	3	4	5	1	2	3	4	5	6	7	8	1	2	3	4	5	6	7	8	(J1-J2)(J2-J3)
							0		0	0	0	0	0										

REFER TO PRODUCT INSTRUCTION E93-906-1 FOR FURTHER INFORMATION ON NMFC☐
REFER TO TECHNICAL OVERVIEW FOR FURTHER INFORMATION ON NMPC01

NETWORK 90

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AL760273B SHT. 20
CAD FILE- 7J080834

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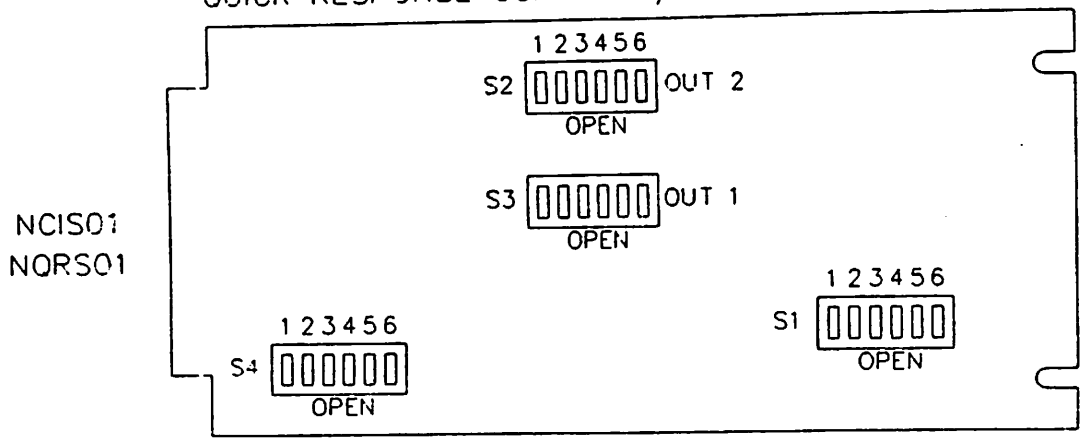
CONTROL I/O SLAVE MODULE SWITCH SETTINGS

QUICK RESPONSE CONTROL I/O SLAVE SWITCH SETTINGS.



1. SWITCH SET OPEN = LOGIC 1
2. S1 – 1 SELECTS D.I. NO.1 VOLTAGE
   S1 – 3 SELECTS D.I. NO.2 VOLTAGE
   S1 – 5 SELECTS D.I. NO.3 VOLTAGE
   } OPEN = 125VDC, CLOSED = 24VDC
   S1 – 2, 4 AND 6 ARE UNUSED
3. ANALOG OUTPUT OPTIONS (S2 = AO NO. 2, S3 = AO NO. 1):
   POS'N 1 –3 = CURRENT = 101
   VOLTAGE =010
   POS'N 4 = SLAVE FAULT DEFAULT VALUE (CLOSED = 0%, OPEN = 100%)
   POS'N 5 = TIME–OUT OPTION (OPEN = HOLD, CLOSED = POWER–UP)
   POS'N 6 = POWER–UP STATE (OPEN = 100%, CLOSED = 0%)
4. S4 IS SLAVE ADDRESS (POS'N 6 = LSB)

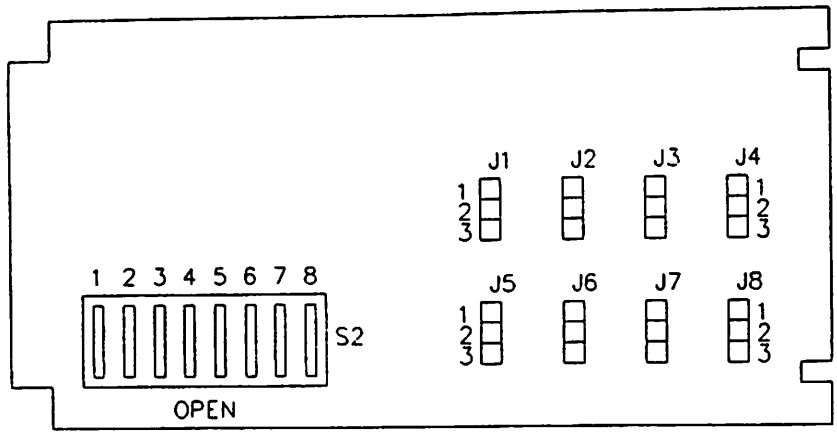
NOTE: DO NOT USE ADDRESS ZERO

MASTER ADDRESS		POSITIONS					
SWITCH	USAGE	1	2	3	4	5	6
S1	D.I. VOLTAGE		X		X		X
S2	A.O. NO.2 OPTIONS						
S3	A.O. NO.1 OPTIONS						
S4	SLAVE ADDRESS						
SLAVE LOCATION							

NOTE: USE ONLY ADDRESS 1 TO 63

REFER TO PRODUCT INSTRUCTION E93–913–9 FOR FURTHER INFORMATION

ENHANCED LOGIC MASTER MODULE SWITCH SETTINGS



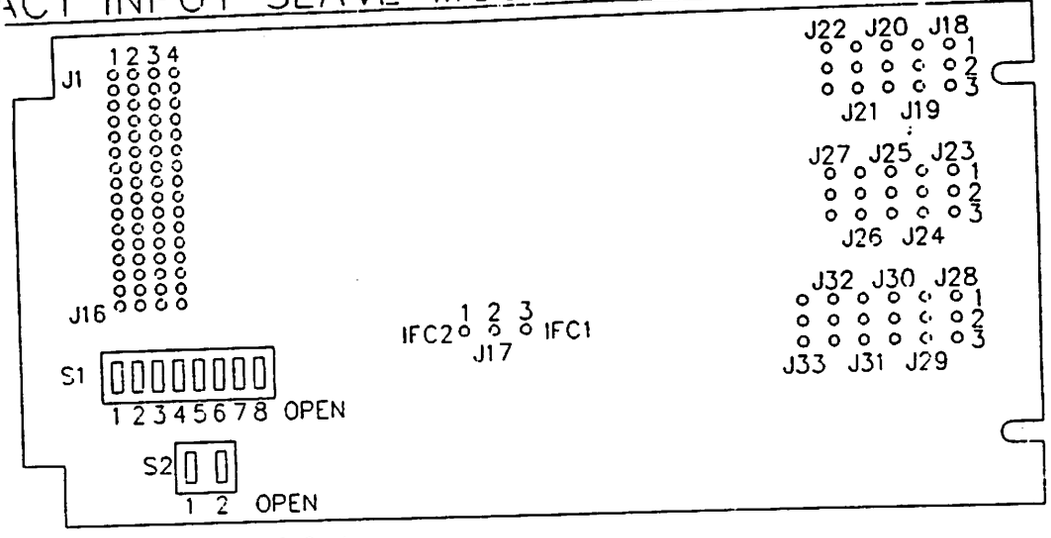
NOTES:

1. SWITCH SET OPEN = LOGIC 1
2. SW2--4 TO 8 IS MODULE ADDRESS (POS. 8 IS LEAST SIGNIFICANT BIT)
3. SW2--1 TO 3 IS MODULE SELF TESTS:
   NORMALLY ALL SET CLOSED.
4. SW2-2 OPEN INITIALIZES EEPROM.
   SW2--2 AND S2-3 OPEN COMPRESSES EEPROM.
5. JUMPERS J1 TO J8 SELECT DIGITAL INPUT VOLTAGE:
   1-2 = 125VDC/120VAC
   2-3 = 24VDC

MASTER ADDRESS	MASTER LOC	S2								J1	J2	J3	J4	J5	J6	J7	J8
		1	2	3	4	5	6	7	8	1/3	1/3	1/3	1/3	1/3	1/3	1/3	1/3
		0	0	0													

REFER TO PRODUCT INSTRUCTION E93-907-5 FOR FURTHER INFORMATION

CONTACT INPUT SLAVE MODULE SWITCH SETTINGS



NOTES:

1. SWITCH SET OPEN = LOGIC 1
2. J1 TO J16 SELECTS POINT RESPONSE TIME: 1 – 2 = D.C. SLOW
   2 – 3 = D.C. FAST
   3 – 4 = A.C.
3. J17 SELECTS SLAVE USAGE: 1–2 = MFC/LMM02 (IFC2),
   2 – 3 LMM01/01A (IFC1)
4. J18 TO J33 SELECTS POINT VOLTAGE: 1 – 2 = 24VDC.
   2 – 3 = 125VDC/120VAC
5. S1 SELECTS SLAVE ADRRESS
   A. IF LMM01/01A – GROUP "A" = POS'N 1 – 4 (4 = LSB)
   – GROUP "B" = POS'N 5 – 8 (8 = LSB)
   B. IF MFC/LMM02 – USE POS'N 3 TO 8 (8 = LSB)
6. S2 SELECTS GROUP ENABLE – A. IF LMM01/01A POS'N 1= GROUP "A"
   POS'N 2= GROUP "B" } OPEN= ENABLE
   – B. IF MFC/LMM02 POS'N 1= NOT USED
   POS'N 2= OPEN

MASTER ADDRESS			SLAVE LOCATION							
RESPONSE TIME	J1	J2	J3	J4	J5	J6	J7	J8		
	J9	J10	J11	J12	J13	J14	J15	J16		
POINT VOLTAGE	J18	J19	J20	J21	J22	J23	J24	J25		
	J26	J27	J28	J29	J30	J31	J32	J33		
SLAVE USAGE	J17	ADDRESS S1	1	2	3	4	5	6	7	8
									S2	1 2

NOTE: USE ONLY ADDRESS 1 TO 63 (FOR MFC/LMM02)

REFER TO PRODUCT INSTRUCTION E93-913-5 FOR FURTHER INFORMATION

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AL760273A SHT. 23
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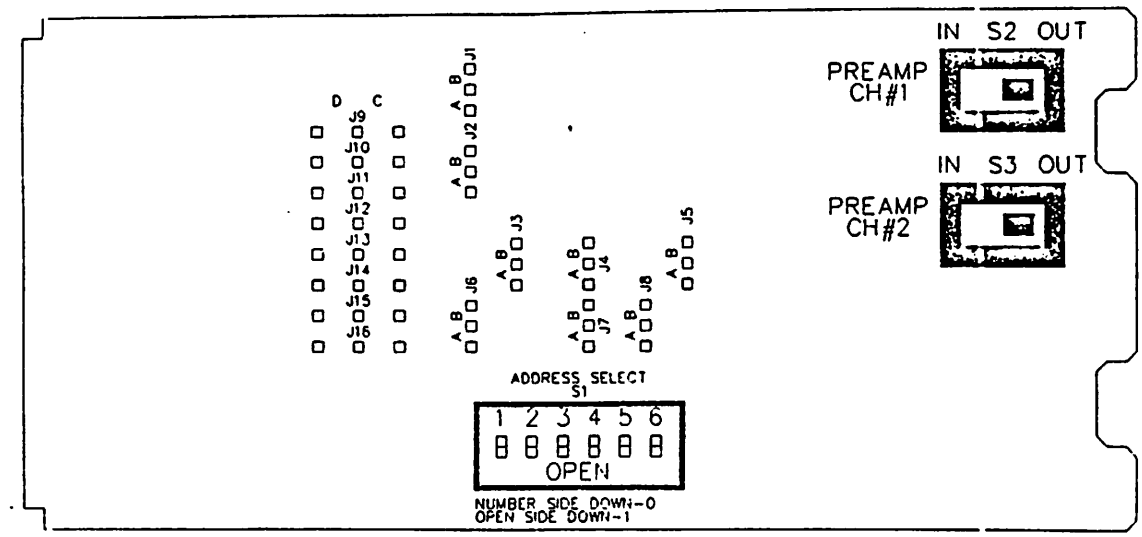
PULSE INPUT SLAVE MODULE

NDSM04



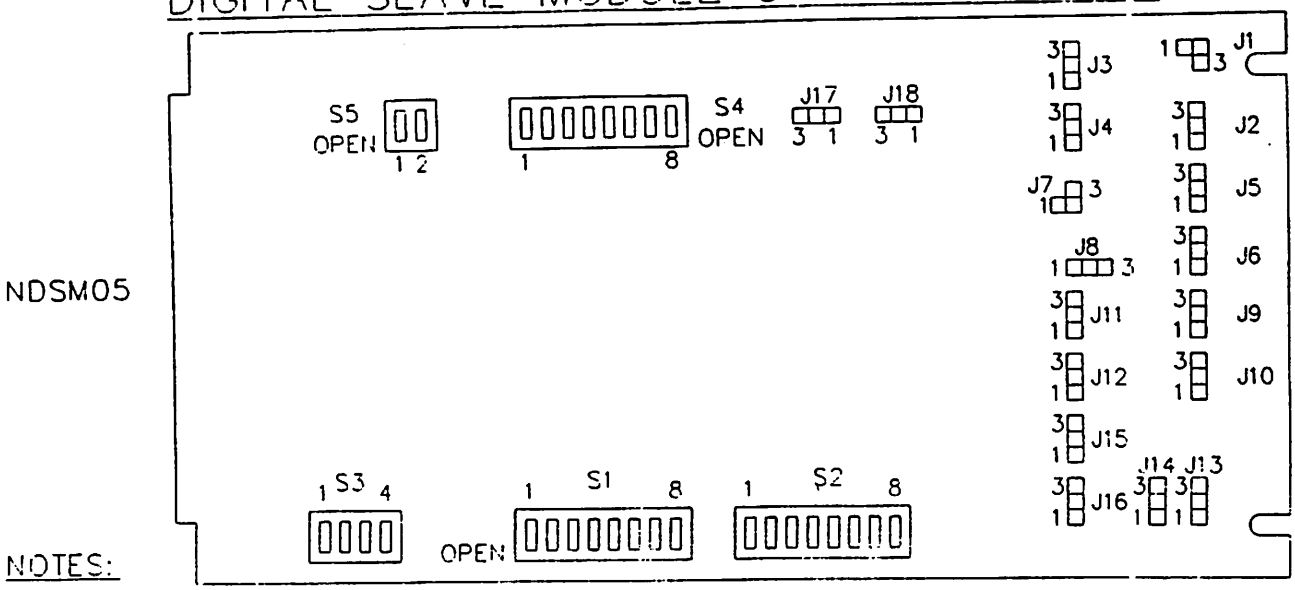
NOTES:

1. SWITCH SET OPEN = LOGIC 1
2. S1 SELECTS SLAVE ADDRESS; 6 = LSB.(USE ONLY ADDRESS 1 TO 63)
3. S2 -IN =ROUTES CHANNEL 1 INPUTS THROUGH PRE-AMP.
   -OUT = CHANNEL 1 INPUTS BYPASS PRE-AMP.
4. S3 -IN =. ROUTES CHANNEL 2 INPUTS THROUGH PRE-AMP.
   -OUT = CHANNEL 2 INPUTS BYPASS PRE-AMP.
5. J1 TO J8 SELECT VOLTAGE RANGE FOR INPUTS.(CH. 1 TO 8 RESPECTIVELY)
   A = LOGIC 0 = 0 TO 1 VDC; LOGIC 1 = 4 TO 6 VDC
   B = LOGIC 0 = 0 TO 2 VDC; LOGIC 1 = 21.6 TO 27 VDC
6. J9 TO J18 SELECT DEBOUNCE FOR INPUTS(CH.1 TO 8 RESPECTIVELY)
   C =NO DEBOUNCE
   D = 8.5mSEC RESPONCE DELAY.

MASTER ADDRESS																											
SLAVE LOC	SLAVE ADDRESS	S1						S2	S3	J1	J2	J3	J4	J5	J6	J7	J8	J9	J10	J11	J12	J13	J14	J15	J16
		1	2	3	4	5	6	%out	%out	A/B	A/B	A/B	A/B	A/B	A/B	A/B	A/B	C/D	C/D	C/D	C/D	C/D	C/D	C/D	C/D

REFER TO PRODUCT INSTRUCTION E93-913-6 FOR FURTHER INFORMATION.

DIGITAL SLAVE MODULE SWITCH SETTINGS



NOTES:

SWITCH SET OPEN = LOGIC 1

1. J1 TO J16 SELECT TYPE OF SIGNAL (3 = ISOLATED OUTPUT, 1 = OTHER)
2. J17 AND J18 SELECT INTERFACE TYPE (1 = LMM01/01A, 3 = MFC/LMM02)
3. S1 SELECTS DEFAULT VALUES FOR GROUP "A" POINTS
   S2 SELECTS DEFAULT VALUES FOR GROUP "B" POINTS
   (0 = DE-ENERGIZE, 1 = ENERGIZE)
4. S3-1 SELECTS I/O TYPE FOR GROUP "A" POINTS
   S3-2 SELECTS I/O TYPE FOR GROUP "B" POINTS
   } 0 = INPUT, 1 = OUTPUT
   S3-3 SELECTS THE MODULE FUNCTION (0 = I/O, 1 = DIGITAL LOGIC STATION)
   (IF S3-3=1 THEN S3-1 AND S3-2 MUST = 1 AND S3-4 MUST = 0.)
   S3-4 SELECTS THE I/O DEFAULT (0 = PRESENT VALUES, 1 = HOLD OUTPUTS)
5. S4 SELECTS MODULE ADDRESS:
   A. IF LMM01/01A - POS'N 1 TO 4 = GROUP A (4 = LSB)
      POS'N 5 TO 8 = GROUP B (8 = LSB)
   B. IF MFC/LMM02 - USE POS'N 3 TO 8 (8 = LSB)

NOTE:	DO NOT USE SLAVE ADDRESS ZERO WITH MFC/LMM02 INTERFACE.

6. FOR LMM01/01A S5-1 ACTIVATES GROUP "A" POINTS
   S5-2 ACTIVATES GROUP "B" POINTS
   } 0 = DISABLE, 1 = ENABLE
   FOR LMM02/MFC S5-2 ACTIVATES ALL POINTS.

MASTER ADD																													
SLAVE LOC	GR 'A' ADD	GB 'B' B	S4								S3				S1								S2							
			1	2	3	4	5	6	7	8	1	2	3	4	1	2	3	4	5	6	7	8	1	2	3	4	5	6	7	8

J1	J2	J3	J4	J5	J6	J7	J8	J9	J10	J11	J12	J13	J14	J15	J16	J17	J18
1/3	1/3	1/3	1/3	1/3	1/3	1/3	1/3	1/3	1/3	1/3	1/3	1/3	1/3	1/3	1/3	1/3	1/3

REFER TO PRODUCT INSTRUCTION E93-913-7 FOR FURTHER INFORMATION

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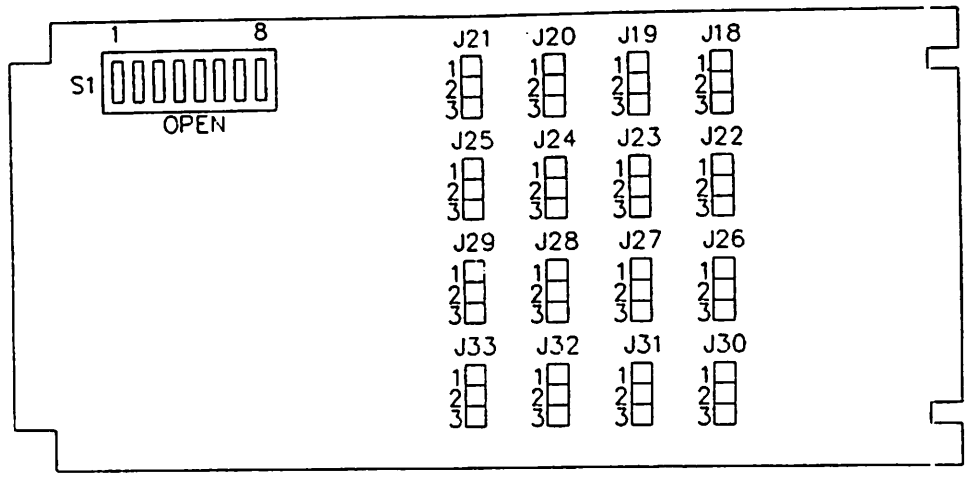
AL760273A SHT. 25
CAD FILE - 7J080838

25

DIGITAL SLAVE INPUT MODULE SWITCH SETTINGS



NDSI01

NOTES:

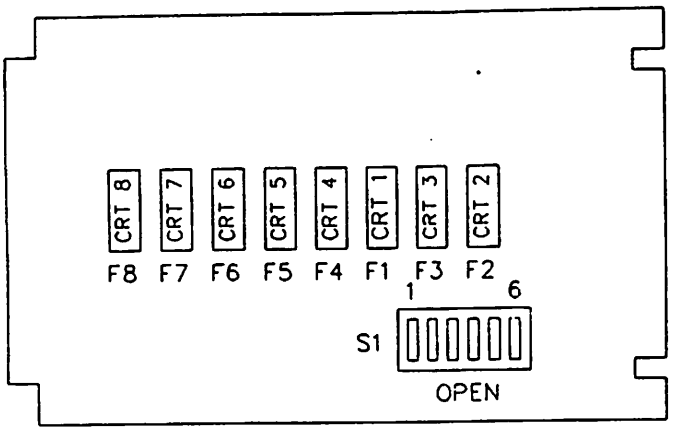
1. SWITCH SET OPEN = LOGIC 1
2. S1 SELECTS SLAVE /GROUP ADDDRESSES.
   FOR LMM01/01A S1-1 TO S1-4 = GROUP A ADDRESS (4=LSB)
   S1-5 TO S1-8 = GROUP B ADDRESS (8=LSB)
   FOR LMM02/MFC S1-1 AND S1-2 MUST BE CLOSED
   S1-3 TO S1-8 = SLAVE ADDRESS (8=LSB)
3. J18 TO J33 SELECTS INPUT VOLTAGE LEVEL
   (3 = 125VDC, 120VAC, 1 = 24VDC)
4. NDSI01 MUST HAVE PIGGY BACK BOARD INSTALLED (KIT #258355) WHEN WORKING WITH AN NLMM01 OR NLMM01A.

MASTER ADDRESS																											
SLAVE LOC	SLAVE ADD	S1								JP18 TO JP33 (1 OR 3)																	
		1	2	3	4	5	6	7	8	18	19	20	21	22	23	24	25	26	27	28	29	30	31	32	33		

REFER TO PRODUCT INSTRUCTION E93-913-15 FOR FURTHER INFORMATION

DIGITAL SLAVE OUTPUT MODULE SWITCH SETTINGS

NDSO01/02/03

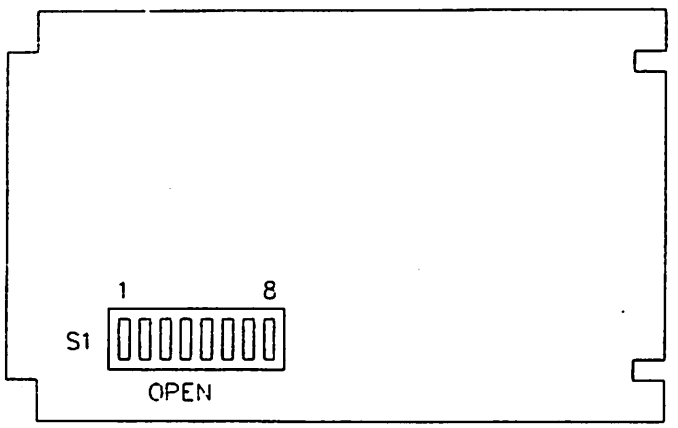


MODULE	OUTPUT TYPE
NDSO01	24VAC–240VAC
NDSO02	4VDC–50VDC
NDSO03	5VDC–160VDC

NOTES:

1. SWITCH SET OPEN = LOGIC 1
2. S1 SELECT ADDRESS; 6 = LSB (ADDRESS 1 TO 63 ONLY)
3. F1 TO F8 FOR NDSO01 – 7.0 AMP SLOW BLOW
   FOR NDSO02 – 4.0 AMP NORMAL
   FOR NDSO03 – 1.5 AMP NORMAL

NDSO04



MODULE	OUTPUT TYPE
NDSM04	24VDC

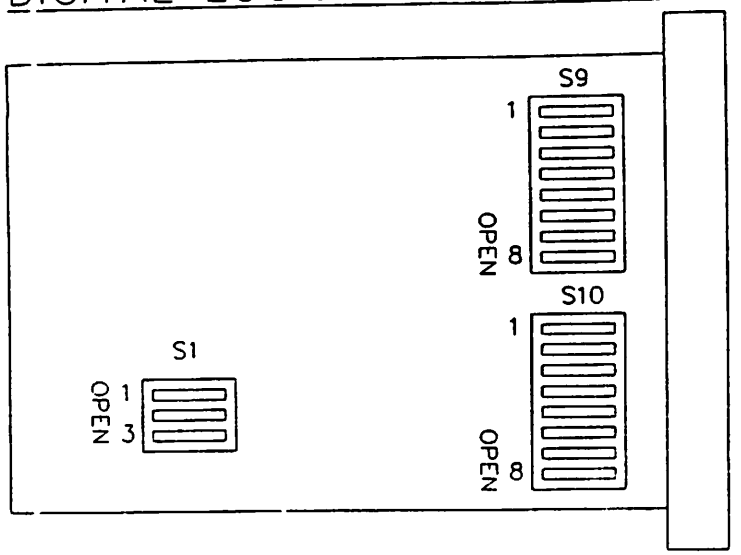
NOTES:

1. SWITCH SET OPEN = LOGIC 1
2. S1 SELECT ADDRESS; 8 = LSB (ADDRESS 1 TO 63 ONLY)

MASTER ADDRESS																
SLAVE TYPE	SLAVE LOC	SLAVE ADD	SW1								F1 TO F8							
			1	2	3	4	5	6	7	8	F1	F2	F3	F4	F5	F6	F7	F8

REFER TO PRODUCT INSTRUCTION E93-913-10 FOR FURTHER INFORMATION

DIGITAL LOGIC STATION SWITCH SETTINGS



NOTE: 1) SWITCH OPEN = LOGIC 1 (POS 3 IS THE LEAST SIGNIFICANT BIT)
2) S1 IS SET TO THE STATION ADDRESS (0 TO 7)
3) S9 & S10 SELECTS INTENSITY DEPENDING ON COLOR LED'S INSTALLED
OPEN=RED/YELLOW CLOSED=GREEN

S9 POS		S10 POS	
1	CR2	1	CR10
2	CR3	2	CR11
3	CR4	3	CR12
4	CR5	4	CR13
5	CR6	5	CR14
6	CR7	6	CR15
7	CR8	7	CR16
8	CR9	8	CR17

4) LED'S ARE RED ON STANDARD MODULE. LED'S MUST BE CHANGE TO OBTAIN OTHER COLORS.

TAG NO.	STATION SERVICE	STA ADD	S1			S9								S10								TU LOC
			1	2	3	1	2	3	4	5	6	7	8	1	2	3	4	5	6	7	8	

REFER TO PRODUCT INSTRUCTION FOR FURTHER INFORMATION NETWORK 90

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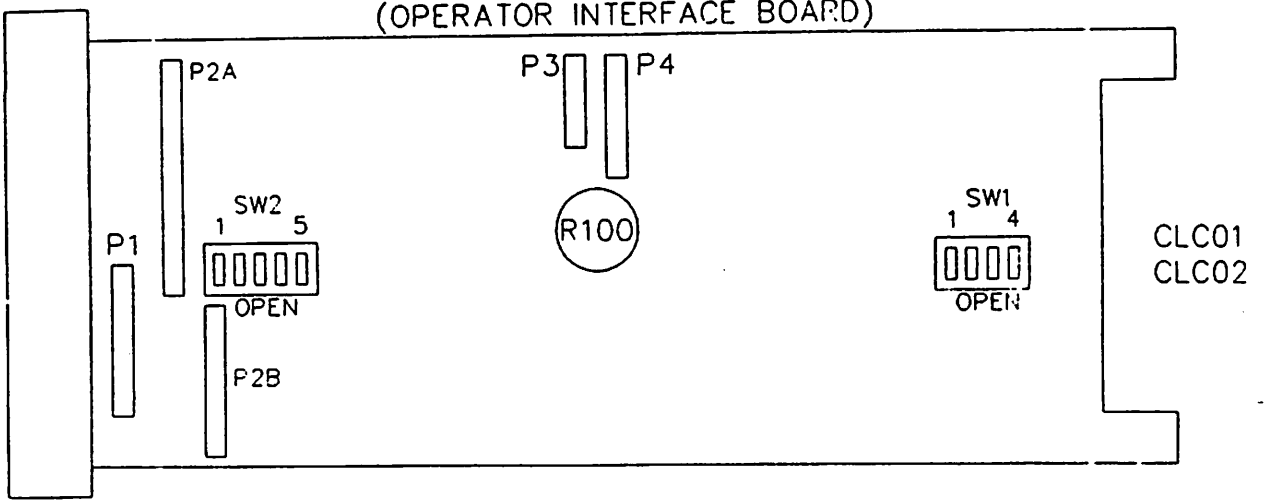
AL760273A SHT. 28
CAD FILE – 7J080833

7/N00175

LOOP COMMAND CONTROLLER SWITCH SETTINGS

(OPERATOR INTERFACE BOARD)



NOTES:

1. SWITCH OPEN = LOGIC 1
2. R100 ADJUSTS BARGRAPH INTENSITY (ADJUST IN POWER-UP CONDITION)
   DECREASE INTENSITY – ADJUST CLOCKWISE
   INCREASE INTENSITY – ADJUST COUNTERCLOCKWISE
3. SW1 OPTIONS

OPTION	SETTING	DESCRIPTION	CUSTOMER SETTING
ANALOG INPUT DISPLAY	SW1-2,3 CLOSED SW1-1,4 OPEN	ANALOG INPUT TO BE GENERATED EXTERNALLY IN MANUAL OVERRIDE MODE.	
	SW1-2,3 OPEN SW1-1,4 CLOSED	ANALOG INPUT TAKEN FROM ANALOG OUTPUT OF INTERFACE BOARD IN MANUAL OVERRIDE MODE.	

SEE DWG. AL105033 SHT. 30 FOR MORE DETAILS.

REFER TO PRODUCT INSTRUCTION E92--500-1 FOR FURTHER INFORMATION

LOOP COMMAND CONTROLLER SWITCH SETTINGS
(OPERATOR INTERFACE BOARD)

NOTES: (CON'T)

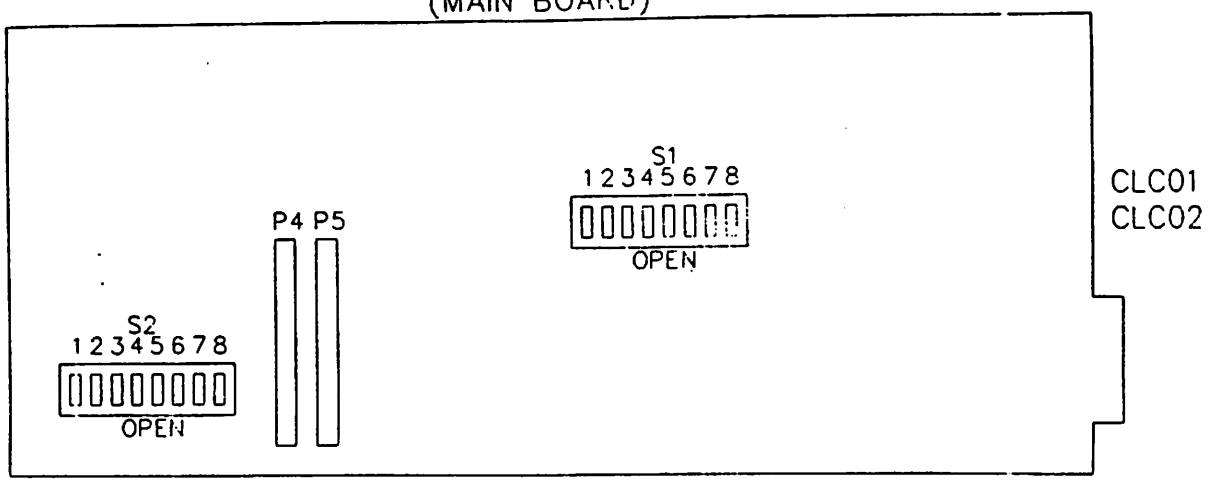
4. SW2 OPTIONS

OPTION	SETTING		DESCRIPTION	CUSTOMER SETTING
DISPLAY TEST	SW2-5 SW2-2	CLOSED CLOSED	CONTROLLER FRONT DISPLAY WILL SEQUENCE THROUGH A GROUP OF INITIAL TEST DISPLAYS.	
	SW2-5 SW2-2	OPEN OPEN	TEST DISPLAY DISABLED.	
CUSTOMER DEFINABLE TAG	SW2-5 SW2-1	CLOSED CLOSED	CUSTOMER CAN PROGRAM LOOP TAGS AND ENGINEERING UNITS FOR PROCESS VARIABLE, AND INTERNAL/ EXTERNAL SET POINTS.	
	SW2-5 SW2-1	OPEN OPEN	CUSTOMER TAG AND ENGINEERING UNITS PROGRAMMING DISABLED.	
NORMAL MODE	SW2-1	CLOSED	IN NORMAL MODE, ANALOG INPUT No.3 IS DISPLAYED ON THE OUT SCALE.	
		OPEN	IN NORMAL MODE, THE CONTROL OUTPUT IS DISPLAYED ON THE OUT SCALE.	
MANUAL OVERRIDE	SW2-2	CLOSED	IN MANUAL OVERRIDE, AN ANALOG INPUT APPEARS ON THE VAR SCALE.	
		OPEN	IN MANUAL OVERRIDE, AN ANALOG INPUT APPEARS ON THE OUT SCALE.	
REVERSE ACTING	SW2-3	CLOSED	REVERSE ACTION IN EFFECT. CAUSES A REVERSE DISPLAY OF THE OUTPUT BARGRAPH AND REVERSES THE DIRECTION OF THE MANUAL OVERRIDE CURRENT OUTPUT	
		OPEN	NO REVERSE ACTION.	
AUDIBLE ANNUNCIATION	SW2-4	OPEN	AUDIBLE ANNUNCIATION (BEEP) PRESENT WHEN ALARM CONDITION EXISTS.	
		CLOSED	DISABLES AUDIBLE ANNUNCIATOR (BEEP)	

REFER TO PRODUCT INSTRUCTION E92-500-1 FOR FURTHER INFORMATION

LOOP COMMAND CONTROLLER SWITCH SETTINGS

(MAIN BOARD)



NOTES:

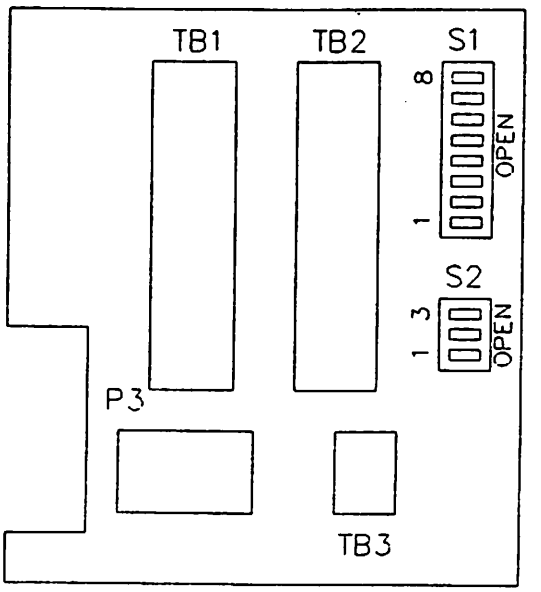
1. SWITCH OPEN = LOGIC 1
2. S2 – 4 TO 8 SELECTS ADDRESS (8 = LSB)
   S2 – 1 – CLOSED = NORMAL OPERATION
   OPEN = FACTORY TEST ENABLED
   S2 – 2 – OPEN = EEPROM INITIALIZED
   CLOSED = NORMAL OPERATION
   S2 – 3 – OPEN = GENERAL PURPOSE SWITCH (HIGH)
   CLOSED = GENERAL PURPOSE SWITCH (LOW)
   (REFERENCE EXECUTIVE BLOCK 240 AND FUNCTION CODE 53 IN FUNCTION CODE MANUAL E93-900-20)
3. S1 – ANALOG OUTPUT OPTIONS

OPTIONS	SETTING	DESCRIPTION	CUSTOMER SETTING
ANALOG OUTPUT No.1	S1-1 OPEN	YEILDS 5.25V OUTPUT FOR POWER UP.	
	S1-1 CLOSED	YIELDS 0.75V OUTPUT FOR POWER UP.	
	S1-2 OPEN S1-3 CLOSED	HOLDS LAST ANALOG OUTPUT VALUE ON TIMEOUT.	
	S1-2 CLOSED S1-3 OPEN	GOES TO POWER-UP VALUE SET BY S1-1 ON TIMEOUT.	
ANALOG OUTPUT No.2	S1-4 OPEN	YEILDS 5.25V OUTPUT FOR POWER UP.	
	S1-4 CLOSED	YIELDS 0.75V OUTPUT FOR POWER UP.	
	S1-5 OPEN S1-6 CLOSED	HOLDS LAST ANALOG OUTPUT VALUE ON TIMEOUT.	
	S1-5 CLOSED S1-6 OPEN	GOES TO POWER-UP VALUE SET BY S1-4 ON TIMEOUT.	

REFER TO PRODUCT INSTRUCTION E92-500-1 FOR FURTHER INFORMATION

LOOP COMMAND CONTROLLER SWITCH SETTINGS

(TERMINATION BOARD)



CLC01
CLC02

NOTES:

1. SWITCH OPEN = LOGIC 1
2. S2 OPTIONS

OPTION	SETTING		DESCRIPTION	CUSTOMER SETTING
DIGITAL INPUT No.1	S2-3	OPEN CLOSED	SET FOR ISOLATED INPUT SET FOR NON-ISOLATED INPUT	
DIGITAL INPUT No.2	S2-2	OPEN CLOSED	SET FOR ISOLATED INPUT SET FOR NON-ISOLATED INPUT	
DIGITAL INPUT No.3	S2-1	OPEN CLOSED	SET FOR ISOLATED INPUT SET FOR NON-ISOLATED INPUT	

SEE DWG. AL105033 SHT. 33 FOR MORE DETAILS.

REFER TO PRODUCT INSTRUCTION E92-500-1 FOR FURTHER INFORMATION

LOOP COMMAND CONTROLLER SWITCH SETTINGS
(TERMINATION BOARD)

NOTES: (CON'T)

3. S1 OPTIONS

OPTION	SETTING		DESCRIPTION	CUSTOMER SETTING
ANALOG INPUT No.1	S1-10	OPEN CLOSED	FOR VOLTAGE INPUT. FOR CURRENT INPUT.	
ANALOG INPUT No 2	S1-9	OPEN CLOSED	FOR VOLTAGE INPUT. FOR CURRENT INPUT.	
ANALOG INPUT No.3	S1-8	OPEN CLOSED	FOR VOLTAGE INPUT FOR CURRENT INPUT.	
ANALOG INPUT No.4	S1-7	OPEN CLOSED	FOR VOLTAGE INPUT. FOR CURRENT INPUT.	
OPERATOR INTERFACE DISPLAY OPTION	*S1-6 & S1-5	OPEN CLOSED	DISPLAY OPTION ON THE MAIN OPERATOR INTERFACE BOARD USING ANALOG INPUT No.3 IS NON-ACTIVATED. DISPLAY OPTION ON THE MAIN OPERATOR INTERFACE BOARD USING ANALOG INPUT No.3 IS ACTIVATED.	
SECONDARY UNIT DISPLAY OPTION	+S1-4 & S1-3	OPEN CLOSED	DISPLAY OPTION OF A SECONDARY UNIT DISPLAY STATION USING ANALOG INPUT No.4 IS NON-ACTIVATED. DISPLAY OPTION OF A SECONDARY UNIT DISPLAY STATION USING ANALOG INPUT No.4 IS NON-ACTIVATED.	
ANALOG OUTPUT No.1	S1-2	OPEN CLOSED	FOR CURRENT OUTPUT. FOR VOLTAGE OUTPUT.	
ANALOG OUTPUT No.2	S1-1	OPEN CLOSED	FOR CURRENT OUTPUT. FOR VOLTAGE OUTPUT.	

* SEE ANALOG INPUT DISPLAY OPTION ON SHEET 29 FOR ADDITIONAL SWITCH SETTINGS ON THE INTERFACE BOARD.

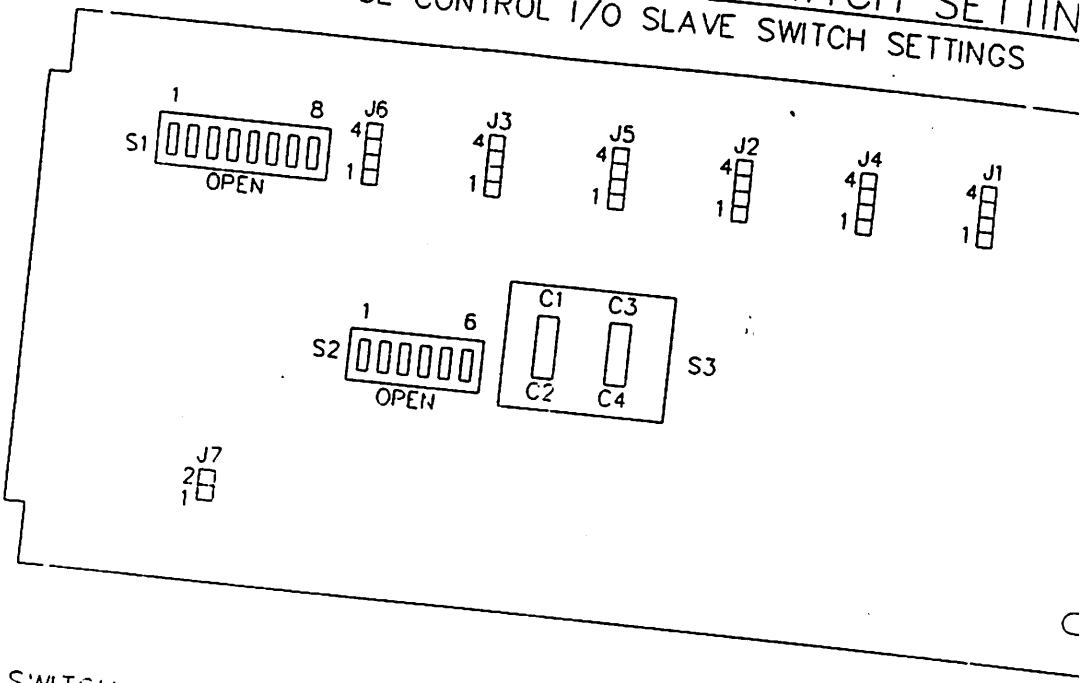
+ SEE SHEETS 13 AND 14 FOR ADDITIONAL SWITCH SETTINGS ON THE NDCS03.

REFER TO PRODUCT INSTRUCTION E92-500-1 FOR FURTHER INFORMATION

CONTROL I/O SLAVE MODULE SWITCH SETTIN

QUICK RESPONSE CONTROL I/O SLAVE SWITCH SETTINGS

NCIS02
NORS02



1. SWITCH SET OPEN = LOGIC 1
2. S1-4 TO 8 SELECT ADDRESS (POS'N 8 = LSB)

NOTE: DO NOT USE ADDRESS ZERO

3. S1 - 1 TO 3 NOT USED
4. ANALOG OUTPUT OPTIONS

OPTIONS	DEFAULT	TIMEOUT OPTION	POWER-UP STATE	OUTPUT TYPE
ANALOG o/p 1	S2-1 CLOSED= 0% S2-1 OPEN =100%	S2-2 CLOSED=POWER-UP S2-2 OPEN =HOLD	S2-3 CLOSED = 0% S2-3 OPEN =100%	C1=CURRENT C2=VOLTAGE
ANALOG o/p 2	S2-4 CLOSED= 0% S2-4 OPEN =100%	S2-5 CLOSED=POWER-UP S2-5 OPEN =HOLD	S2-6 CLOSED = 0% S2-6 OPEN =100%	C3=CURRENT C4=VOLTAGE

SEE DWG AL760273 SHT. 35 FOR MORE DETAILS.